

Electronic Acknowledgement Receipt

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Application Number:	10659974
International Application Number:	
Confirmation Number:	5045
Title of Invention:	CHANNEL BONDING OF A PLURALITY OF MULTI-GIGABIT TRANSCEIVERS
First Named Inventor/Applicant Name:	Joseph Neil Kryzak
Customer Number:	24309
Filer:	Justin Liu/Katherine Stofer
Filer Authorized By:	Justin Liu
Attorney Docket Number:	X-1367 US
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Payment Type	Deposit Account
Payment was successfully received in RAM	\$810
RAM confirmation Number	4119
Deposit Account	240040
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Foreign Reference	EP_0315275_A2.pdf	818323 73f317b0448bcb3e9c21c1061b041b1a52503d	no	12
Warnings:					
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2	Foreign Reference	WO_9325968A1.pdf	1145636 5c1d69db1634835be43d8ce14482eb01b6e00b	no	30
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3	Foreign Reference	EP_0905906A2.pdf	502067 65f533d5778bcb4b23e27373aa348be7c29c1a9	no	10
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4	Foreign Reference	EP_1235351A1.pdf	505729 04b5251e02b6872f6304b97a4add1270e150b4	no	11
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5	NPL Documents	Albahama_Area_and_Time_NPL.pdf	501011 9f15915bd22c0f5ab15e1a114c891b081f4d5b4	no	6
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6	NPL Documents	Andrew_A_Field_Prog_NPL.pdf	386491 6398605495071ddeb5e68b7140c8512f5da557e	no	5
Warnings:					
Information:					
7	NPL Documents	Carter_The_Future_of_Programmable_NPL.pdf	586523 210d3c698cecb49d51ab03961fbcb78b76c0d35d	no	8
Warnings:					
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8	NPL Documents	Davidson_FPGA_Implementation_NPL.pdf	232306 524ab3d0f7edf89a20d339a85410805572bec79	no	4
Warnings:					
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9	NPL Documents	DeHon_DPGA_Coupled_NPL.pdf	798451 8968c93205e94b234ed34e21b8c0d2743218e374	no	10
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10	NPL Documents	French_A_Self_reconfiguring_NPL.pdf	813885 b9ce5be4dc79ed9d424d5120c2ba4eb1a d7b2b225	no	11
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11	NPL Documents	IBM_32Bit_NPL.pdf	6761375 793b23adca8b45815a28e44ee1d1bdc5 e59b2a8c	no	140
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12	NPL Documents	IBM_Powerpc_405_NPL.pdf	18215204 e060956c9c089cc3059c5c27cb91b09 120c2c1	no	550
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13	NPL Documents	Iseli_A_C_Plus_Plus_Compiler_NPL.pdf	446551 9fa4c113130224b55e1a8b75b52b3dc 71a54792	no	8
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14	NPL Documents	Iseli_Beyond_Superscaler_NPL.pdf	255066 d238f4c52b2c2762348270a1b3b3a54 90265e29	no	5
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15	NPL Documents	Iseli_Spyder_A_Reconfigurable_NPL.pdf	442425 2994ae17276c448e52b5aa3cabe792b7 f1b1ee79	no	8
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16	NPL Documents	Kiaei_VLSI_Design_of_Dynamically_NPL.pdf	270396 ba6a993acc185dc0b4e1c252f1a7a59c 83ce366	no	5
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17	NPL Documents	Maki_A_Reconfigurable_Data_NPL.pdf	256368 a8ae705c19864119a0a80c1b5531bbca b10d2ca48	no	4
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18	NPL Documents	Snyder_Xilinx_A_to_Z_NPL.pdf	490110 9301317a08b382ba141187945eb07795 9b7999d3	no	5
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19	NPL Documents	Srini_Field_Programmable_Gate_NPL.pdf	495908 8633b3fa7e243b32c998344abc1811954e41b68	no	6
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20	NPL Documents	Wirthlin_The_Nano_Processor_NPL.pdf	711475 e0706339dcff8e4a0a43625ac2501bcb3ceff5c3b	no	10
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21	NPL Documents	Wittig_OneChip_an_FPGA_NPL.pdf	1023485 55c6390fccceba4293cab4d532ba5a1d3c60059e	no	11
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22	NPL Documents	Xilinx_Programmable_LDB_1994_2_9_to_2_18_NPL.pdf	1066027 6417954074352c1608020bed12e9815e8ed53482	no	13
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23	NPL Documents	Xilinx_Programmable_LDB_1994_2_107_to_2_108_NPL.pdf	331834 629220c36135db732487199ea5896daf398367c	no	5
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24	NPL Documents	Xilinx_Programmable_LDB_1994_2_109_to_2_117_NPL.pdf	773523 299459636b2270816a3b678be1764e5dd3d326	no	12
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25	NPL Documents	Xilinx_Programmable_LDB_1994_2_187_to_2_199_NPL.pdf	1156817 037a16712246803c0cab852b5a5f7124e183971	no	16
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26	NPL Documents	Xilinx_Programmable_LDB_1999_3_1_to_3_50_NPL.pdf	3031136 800143b28f35dc3d899a49d4689785dbec3721421	no	54
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27	NPL Documents	Xilinx_Programmable_LDB_2000_3_1_to_3_117_NPL.pdf	7056683 664b08fb7e119b0568f0324ac75248a5d328471d	no	122
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28	NPL Documents	Xilinx_Programmable_LDB_2000_3_7_to_3_87_NPL.pdf	2219494 494786481d8d3ed3517299667b55021691ea953	no	27
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29	NPL Documents	Xilinx_Virtex_Two_PLatform_FPGA_Hndbk_NPL.pdf	3105531 34966c995af30c270362818932801b05f5c143b	no	45
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30	NPL Documents	Yamin_Aizup_A_Pipelined_NPL.pdf	501670 0265336bd4a5996a91e1121414c03b59c972a11	no	9
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31	NPL Documents	Xilinx_Programmable_LDB_1994_rv_1995_Part1_pgs_1_to_345_NPL.pdf	21297550 6d731e5733521ccc6965c29f5382cfce43c05a	no	345
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32	NPL Documents	Xilinx_Programmable_LDB_1994_rv_1995_Part2_pgs_346_to_689_NPL.pdf	20651519 c60d5b085770d5ce93963546d23429632e864c7	no	344
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33	Request for Continued Examination (RCE)	X1367_US_RCE_wIDS_efiled.pdf	627252 2251cef1567c824615ac2bc2b1ff59b5f4641e0ef91	no	3
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34	Information Disclosure Statement (IDS) Filed	X1367_US_SB08A_efiled.pdf	130890 5a374aac9289d0f529c1469b4e489240d3730d9d2	no	7
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This is not an USPTO supplied IDS fillable form					
35	Fee Worksheet (PTO-06)	fee-info.pdf	8205 e6047b3b33b35656b595514a328206d812954c	no	2
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Total Files Size (in bytes):			97616916		

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.